

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Juergen Einspenner et al.

Examiner: Nghia M. Doan

Serial No.: 10/528,035

Group Art Unit: 2825

Filed: December 16, 2005

Docket No.: I431.125.101/FIN249PCT/US

Title: METHOD FOR DETERMINING THE ARRANGEMENT OF CONTACT AREAS ON THE ACTIVE TOP SIDE OF A SEMICONDUCTOR CHIP

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is responsive to the Office Action mailed June 7, 2007, in which a Restriction Requirement was set forth relative to the Examiner-identified inventions of Group I (claims 14-22 and 35, drawn to a method for determining the arrangement of contact areas of semiconductor chip), Group II (claims 23-28, drawn to a method for determining the arrangement contact areas on an active top side of semiconductor chip in or on a housing), Group III (claims 29-31 and 36, drawn to a method for creating a bonding plan for an electronic device having a semiconductor chip and having a housing) or Group IV (claims 32-34 and 37, drawn to a method for generating geometry data for the creation of photomask for the exposure of an electronic device having a semiconductor chip having a housing for means of photographic). In response, Applicant hereby elects Group II (claims 23-28, drawn to a method for determining the arrangement contact areas on an active top side of semiconductor chip in or on a housing) without traverse.

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Any inquiry regarding this Response should be directed to Mark L. Gleason at Telephone No. (612) 767-2503, Facsimile No. (612) 573-2005. In addition, all correspondence should continue to be directed to the following address:

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Respectfully submitted,

Juergen Einspenner et al.

By their attorneys,

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